

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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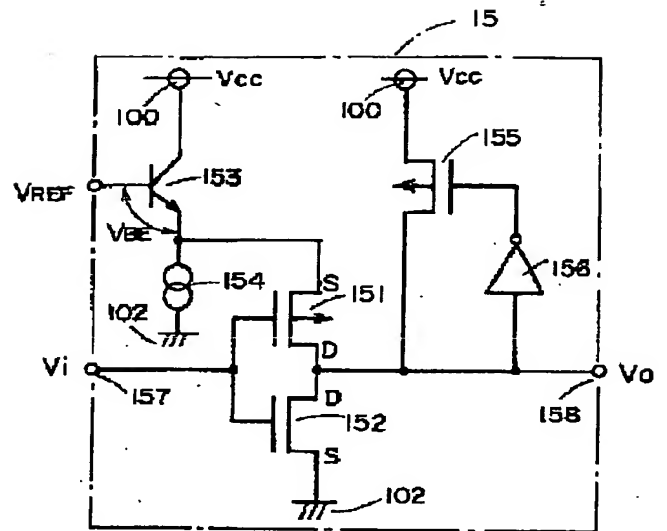
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TITLE : LOGIC CIRCUIT



**ABSTRACT :** **PURPOSE:** To always make the logical threshold value of an input logic circuit constant regardless of the fluctuation of a power supply voltage.

**CONSTITUTION:** A constant voltage is supplied to the source terminal of a transistor 151 constituting an inverter. The constant voltage is generated based on a reference voltage VREF by a transistor 1533 and a current source 154. Also, a transistor 155 and an inverter 156 are connected with an output terminal 158, and a voltage VREF-VBE level supplied to the output terminal 158 is increased to a power supply voltage VCC level.

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